

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Lawrence Salant, et al.
Serial No. : 10/673,712
For : METHOD OF BIT ERROR RATE ANALYSIS
Filed : September 29, 2003
Examiner : Guerrier Merant
Art Unit : 2117
Confirmation No. : 2458

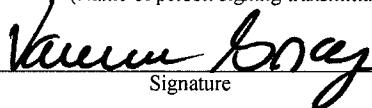
745 Fifth Avenue
New York, NY 10151

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being transmitted
via Electronic Filing Services on June 11, 2008.

Valerie Gray

(Name of person signing transmittal)



Signature

June 11, 2008

Date of Signature

FILING OF REPLACEMENT DRAWINGS

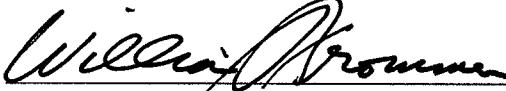
Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowability mailed in the above-identified application on April 7, 2008, please replace the drawings now on file with the attached replacement sheets. No new matter is present.

Respectfully submitted,
FROMMER LAWRENCE & HAUG LLP

By:



William S. Frommer
Reg. No. 25,506
(212) 588-0800